FLX500

Entry Level Desktop Automation



Simplicity

Ease of use with Data I/O's patented graphical user interface

- ✓ No language requirement
- ✓ Real time graphical display
- ✓ Instant learning

Speed

Maximize throughput with blazing fast programming

- ✓ <2 minute changeover</p>
- ✓ Optimized algorithms
- ✓ FlashCORE III & SuperBoost technology
- ✓ Data I/O's perfected dual pick and place probes





FLX500

System Specifications

Features

- 16 programming sockets (4 programmers)
- 600/pph
- Networked operation
- USB 2.0 (job creation via TaskLink)
- LCD touchscreen display and status lights
- Self-learning plug-and-play operation
- Pneumatic system with minimal noise (air facility required)

System Software

- TaskLink for Windows
- Microsoft XP Professional OS in embedded PC

Network Interface

• Network: Gigabit Ethernet

Programmer

FlashCORE III with SuperBoost Technology

Physical Specifications

- **Length:** 770 mm (30.3 inches)
- **Width:** 592 mm (23.3 inches) • **Height:** 615mm (24.2 inches)
- Weight base unit with no modules:
 - 31.75kg (70 lbs)

Value Added Software

- NAND Bad Block Schemes
- Serial Number Server
- Data Mapper
- Version Control and more!

Services

- One year warranty
- Annual Programmer Support (APS): The first year of support is included in the system purchase price and can be extended via annual support agreement renewal. The APS support program covers both hardware and system software (consumables are not included)

Media

• JEDEC and nonstandard trays up to 186 mm x 334 mm

Optional Subsystems

- Standard Tray Module
- Programmer Module

Adapters & Device Support

Socketing Technology

- Standard burn-in sockets (5,000 10,000 insertions per socket)
- High Insertion Count (HIC) for BGA, TSOP, QFP (250,000 insertions per socket) and other packages

Universal Device Support

Flash Memory (NOR, NAND, MCP, MMC, e.MMC, SD, MoviNAND, OneNAND, iNAND, Serial Flash, EEPROM, EPROM and more), Microcontrollers and Logic devices (CPLD, FPGA's, PLD's) and more

Package Support

TSOP, BGA, QFP, TSOP2, FBGA, TQFP, CSP, uBGA, PLCC, SOIC, EBGA, PoP, SSOP, TFBGA and more

Device Programming & Testing

Program, Blank Check, Read, Verify, Illegal Bit Check, Erase, Secure, Pin Continuity and ID Check

Requirements

Electrical/Power

Operating Voltage: 100 to 240 VAC

Power Consumption: <500 watts

• Frequency Range: 50 to 60 Hz

Air

• Minimum 56L/min @ .52 MPa or 2.0 CFM @ 75 PSI

Temperature

• 15° C to $+35^{\circ}$ C (59° F to $+95^{\circ}$ F)

Humidity

• 5-90% non-condensing

PC Workstation for TaskLink

Operating System: Windows XP or Windows 7

Hard disk space: 3GB plus space for device data (up to 32 GB)

 USB 2.0 port: for jobs (or network) Network interface: recommended 100Base T

CD ROM Drive

Serial or USB mouse

 VGA monitor: 640 x 480 minimum resolution





